Advance Information N-Channel JFET –25V, 20 to 40mA, 40mS

Automotive JFET designed for compact and efficient designs and including high gain performance. AEC-Q101 qualified JFET and PPAP capable suitable for automotive applications.



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Features

- High Forward Transfer Admittance
- High Breakdown Voltage
- Low Input Capacitance
- Low Noise Figure
- Pb-Free and RoHS compliance
- AEC-Q101 qualified and PPAP capable

Typical Applications

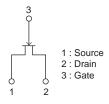
• Low Noise Amplifier for Automotive AM Radio

SPECIFICATIONS ABSOLUTE MAXIMUM RATINGS at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSX}	25	V
Gate-to-Drain Voltage	V _{GDS}	-25	V
Gate Current	IG	10	mA
Drain Current	ID	50	mA
Allowable Power Dissipation	PD	400	mW
Operating Junction and Storage Temperature	T _{J,} T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CONNECTION N-Channel





MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet

This document contains information on a new product. Specifications and information herein are subject to change without notice.

ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

Parameter	Cymphol	Conditions	Value			Unit
Parameter Symbol Conditions		min	typ	max		
Gate-to-Drain Breakdown Voltage	V(BR)GDS	$I_{G} = -10\mu A, V_{DS} = 0V$	-25			V
Gate Cutoff Current	IGSS	$V_{GS} = -10V, V_{DS} = 0V$			-1.0	nA
Cutoff Voltage	VGS(off)	$V_{DS} = 5V, I_{D} = 100 \mu A$	-0.6	-1.2	-1.8	V
Drain Current	IDSS	$V_{DS} = 5V$, $V_{GS} = 0V$	20		40	mA
Forward Transfer Admittance	yfs	$V_{DS} = 5V$, $V_{GS} = 0V$, $f = 1kHz$	30	40		mS
Input Capacitance	Ciss	V _{DS} = 5V, V _{GS} = 0V, f = 1MHz		6.0		pF
Reverse Transfer Capacitance	Crss	VDS = 3V, VGS = 0V, I = 11VII 12		2.3		pF
Noise Figure	NF	$V_{DS} = 5V, V_{GS} = 0V f = 100MHz$		2.1	2.8	dB

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

 $V_{GS}=0V$

-0.2V

-0.4V

-0.6V

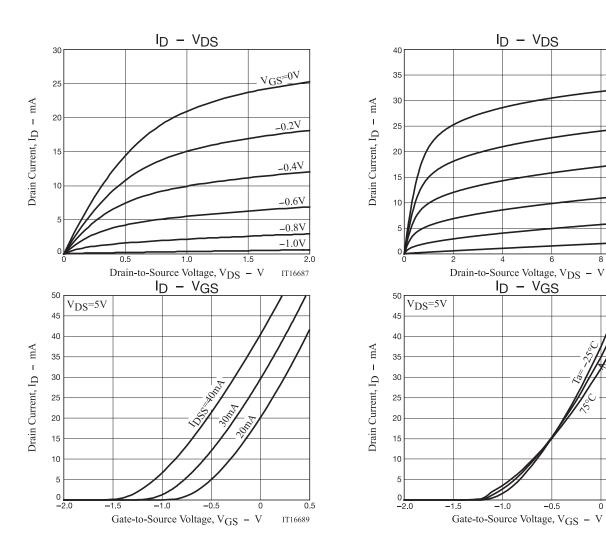
-0.8V

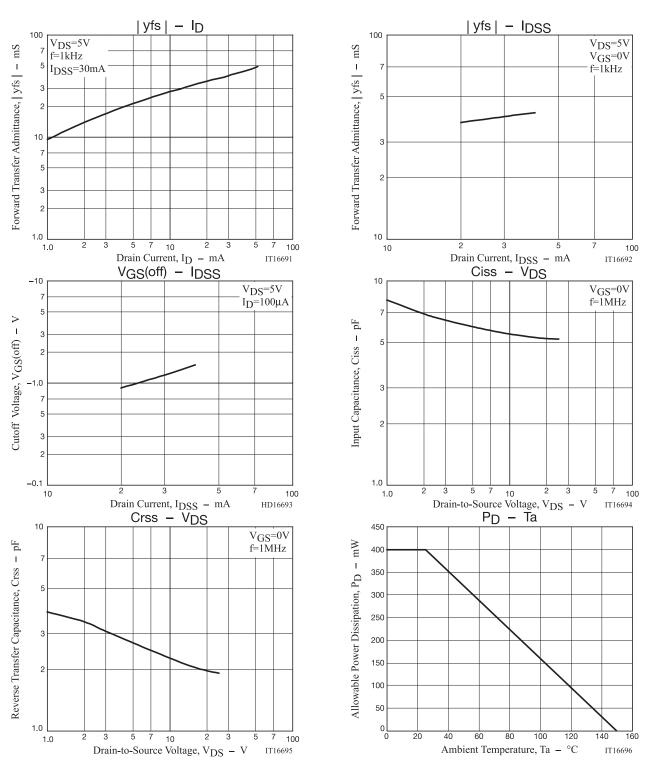
-1.0V

IT16688

0.5

IT16690

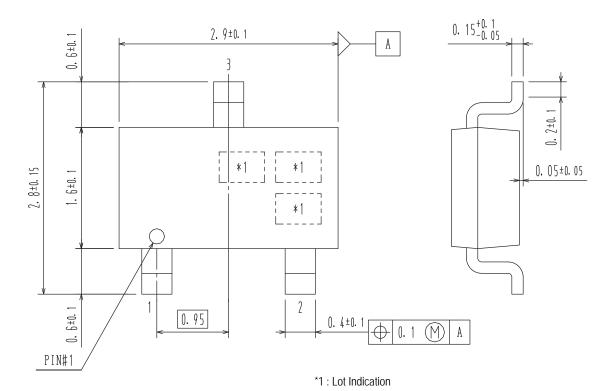




PACKAGE DIMENSIONS

unit: mm

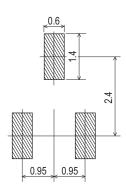
CPH3 CASE 318BA ISSUE O



0.9 40.05

1 : Source 2 : Drain 3 : Gate

RECOMMENDED SOLDERING FOOTPRINT



ORDERING INFORMATION

Device	Marking	Package	Shipping
NSVJ3910SB3T1G	J2	CPH3 (Pb-Free)	3,000 / Tape & Reel

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

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